



2123

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Total Number of Pages in This Submission

Application Number	10/076,920
Filing Date	February 15, 2002
First Named Inventor	Deliwala
Art Unit	2123
Examiner Name	Luke R. Osborne
Attorney Docket Number	53168-500301-D3

**ENCLOSURES (Check all that apply)**

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Remarks

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name Wendy W. Koba, Esq.

Signature *Wendy W. Koba*

Printed name Wendy W. Koba

Date September 24, 2007

Reg. No. 30509

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**EXPRESS MAIL  
CERTIFICATE OF MAILING**

**Patent Application for:**

**Applicants: Shrenik Delilwala**

**Atty. No: 53168-500301-D3**

**Title: Integrated Optical/Electronic Circuits and Associated Method  
of Simultaneous Generation Thereof**

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IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s) Shrenik Deliwala

Case 53168-500301D3

Conf. No. 3947

Serial No. 10/076,920

Group Art Unit 2123

Filing Date February 15, 2002

Examiner Luke R. Osborne

Title Integrated Optical/Electronic Circuits and Associated  
Method of Simultaneous Generation Thereof

COMMISSIONER OF PATENTS  
WASHINGTON, D.C. 20231

SIR:

**STATEMENT UNDER 37 CFR 1.125(a)**

In response to the Office action dated June 29, 2007, applicant is submitting for consideration by the Examiner a substitute specification in accordance with the requirements of 37 CFR 1.125(a). In particular, a marked-up copy showing all amendments made subsequent to the original filing is presented for review, as well as a 'clean copy'. **It is asserted that this substitute specification contains no new subject matter.**

Respectfully submitted,

Shrenik Deliwala

By: Wendy W. Koba  
Wendy W. Koba  
Reg. No. 30509  
Attorney for applicant  
610-346-7112

Date: 9/24/2007